CP/M Plus™
(CP/M® Version 3)
Operating System System Guide
Release Notes

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The following are corrections to the CP/M Plus™ (CP/M® Version 3)
Operating System System Guide.

Page v

After the Table of Contents entry "1.6 Disk Organization", insert
the following missing entry:

   1.7 Hardware Supported

Page vi

The Table of Contents entry "4 The Modules of the CP/M 3 Sample
BIOS Modules" should read as follows:

   4 CP/M 3 Sample BIOS Modules

The entry "4.7.6 Multiple Section I/O" should read as follows:

   4.7.6 Multiple Sector I/O

Page 17

The paragraph immediately below the table should be revised so that
"TIME" is removed from the second sentence and inserted in the
fourth sentence. The paragraph should read as follows:

     You do not need to implement every function in the BIOS
   jump vector. However, to operate, the BDOS needs the BOOT,
   WBOOT, CONST, CONIN, CONOUT, HOME, SELDSK, SETTRK, SETSEC,
   SETDMA, READ, WRITE, SECTRN, MULTIO, and FLUSH subroutines.
   Implement SELMEM and SETBNK only in a banked environment. You
   can implement MULTIO, FLUSH, and TIME as returns with a zero in
   Register A. DEVICE and some other utilities use the remaining
   entry points, but it is not necessary to fully implement them
   in order to debug and develop the system.

Page 22

Table 2-6 is missing the heading "Banked BDOS", which should appear
in the middle of the table after "READ, WRITE" but before "SELDSK".

Page 41

In Table 3-3, under AL0, AL1, the sentence that reads "See Figure 3-
8 for more information" should read as follows:

    See Figure 3-4 for more information.

Page 50

Table 3-10 should be corrected as follows:

    For Function 16, move "DE=Trans Tble Adr" to the Input column.
    For Function 22, delete "HL=0FFFDH" from the Output column.
    For Function 25, add "BC=Count" to the Input column.
    For Function 29, delete "BC=Count" from the Input column.

Page 53

In Function 22, the last two returned values should read as follows:

    HL=0FFFFH if no Drive Table; GENCPM does not
    set up buffers. Hashing is supported.
    HL=0FFFEH if no Drive Table; GENCPM does not
    set up buffers. Hashing is not supported.

Page 59

The paragraph immediately before Function 10 should read as follows:

        SELDSK must return the address of the Disk Parameter
    Header on each call. Postpone the actual physical disk select
    operation until a READ or WRITE is performed, unless I/O is
    required for automatic density-sensing.

Page 64

In the "Note:" immediately before Section 3.4.4, the Register
referred to should be A, not 1A.

Page 85

The section header "4.7.6 Multiple Section I/O" should read as follows:

4.7.6 Multiple Sector I/O

Page 87

The fourth paragraph should read as follows:

GENCPM relocates the system modules, and can allocate physical record buffers, allocation vectors, checksum vectors, and hash tables as requested in the BIOS data structures. It also relocates references to the System Control Block, as described on page 27. GENCPM accepts its command input from a file, GENCPM.DAT, or interactively from the console.

Pages 161, 162

The changes to Page 50 are applicable to these pages too.

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The following pages present corrections to the CP/M Plus™ (CP/M® Version 3) Operating System System Guide.

PAGES 50 AND 161

Function number 22 in Table 3-10 (page 50) and Table L-1 (page 161) is incorrect. In Function 22, DRVTBL, delete this output parameter:

        HL=0FFFDH

PAGE 120

On page 120, lines 243, 256, 291, and 302 are incorrect. Replace them with the following code:

```
243    00E9 0600        mvi b,0      ; start with device 0

256    00FF 04          inr b        ; next device number

291    0115 0600        mvi b,0      ; start with device 0

302    0123 04          inr b        ; next device number
```

PAGE 121

On page 121, lines 371 and 377 are incorrect. Replace them with the following code:

```
371    0180 0600        mvi b,0      ; start with device 0

377    018A 04          inr b        ; next device number
```

PAGE 122

On page 122, lines 402 and 409 are incorrect. Replace them with the following code:

```
402    019C 0600        mvi b,0

409    01A8 04          inr b        ; else, next device
```

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PAGE 137

On page 137, lines 27, 28, 29, and 59 are incorrect. Replace them with the following code:

```
27   0000 2100802200      lxi h,08000h ! shld @civec ! shld @covec     ; assign console to CRT:
28   0009 2100402200      lxi h,04000h ! shld @lovec                   ; assign printer to LPT:
29   000F 2100202200      lxi h,02000h ! shld @aivec ! shld @aovec     ; assign AUX to CRT1:

59   0026 2100010180      lxi h,0100h ! lxi b,0C80h      ; clone 3.125K, just in case
```

PAGE 138

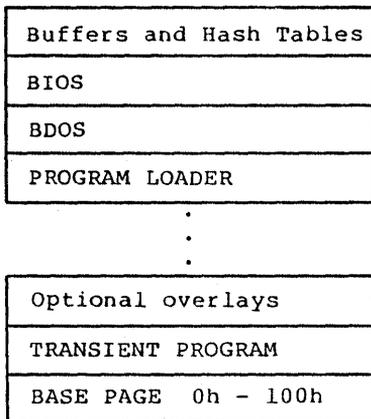On page 138, line 79 is incorrect. Replace with the following code:

```
79   0056 2100010180      lxi h,0100h ! lxi b,0C80h      ; clone 3.125K
```

PAGE 9

Figure 1-6, Memory Organization in Nonbanked CP/M 3, is incorrect. The following Figure 1-6 is correct:

```
+---------------------------------+
|   Buffers and Hash Tables       |
+---------------------------------+
|   BIOS                          |
+---------------------------------+
|   BDOS                          |
+---------------------------------+
|   PROGRAM LOADER                |
+---------------------------------+
                .
                .
                .
+---------------------------------+
|   Optional overlays             |
+---------------------------------+
|   TRANSIENT PROGRAM             |
+---------------------------------+
|   BASE PAGE   0h - 100h         |
+---------------------------------+
```

Figure 1-6. Memory Organization in Nonbanked CP/M 3

CP/M PlusT.M.**(CP/M**® **Version 3)**
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Following are corrections to the CP/M PlusT.M.(CP/M® Version 3)
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Page 82

Section 4.7.3   Extended Disk Parameter Headers (XDPHs)

Figure 4-1., XDPH Format, is incorrect. The Media Flag shown
at Address XDPH+10 should be in the High Byte column, and 0 should
be in the Low Byte column.

Page 86

Section 4.9   Linking Modules into the BIOS

The option shown in the second link command example is
incorrect. The command line should read as follows:

LINK BIOS3[os]=BIOS,SCB,BOOT,CHARIO,MOVE,DRVTBL,<disk_modules>